#19

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Attorney Docket No.: 12172-004530US	Application No.: 09/057,861
	Applicant: Howard G. Sachs	
	Filing Date: April 9, 1998	Group: 2784 2154

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub-class	Translation (Yes/No)

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

VV. AA	Anderson, D.W. et al.[1967] "The IBM 360 model 91: Processor philosophy and instruction handling." IBM J. Research and Development 11:1 (January) pp 8-24.
VV. AB	Charlesworth, A.E., [1981]. "An approach to scientific processing: The architecture design of the AP-120B/FPS-164 family" Computer 14:9 (September), pp. 18-27.
VV. AC	Colwell, R.P. et al. [1987]. "A VLIW architecture for a trace scheduling compiler." Proc. second Conf. on Architectural Support for Programming Languages and Operating Systems, IEFF/ACM (March), pp. 180-192.
VV. AD	Dehnert, J.C. et al. [1989]. "Overlapped loop support on the Cydra 5." Proc. Third Conf. on Architectural Support for Programming Languages and Operating Systems (April), TEEE/ACM. Boston, pp. 26-39.
VV. AE	Rau, B.R., et al. [1989]. "The Cydra 5 departmental supercomputer: Design philosophies, decisions, and tradeoffs," IEEE Computers, 22:1 (January), pp. 12-34
VV. AF	Smith, J.E. [1989]. "Dynamic instruction scheduling and the astronautics ZS-I" Computer 22:7 (July), pp. 21-35
VV. AG	Smith, J.E. et al. [1987]. "The ZS-I central processors," Proc. Second Conf. on Architectural Support for Programming Languages and Operating Systems, IEEE/ACM (March), pp. 199-204.
VV. AH	Sohi, G.S. [1990], "Instruction issue logic for high-performance, interruptible, multiple functional unit pipelined computers," IEEE Trans. on Computers 39:3 (March), 349-359.
VV. AI	Sohi and Vajapeyam [1989]. "Tradeoffs in instruction format design for horizontal architectures," Proc. Second Conf. on Architectural Support for Programming Languages and Operating Systems, IEEE/ACM (April), pp. 15-25.
VV. AJ	Tomasulo, R.M. [1967], "An efficient algorithm for exploiting multiple arithmetic units," IBM J. Research and Development 11:1 (January), 25-33.
VV. AK	Weiss and Smith [1984]. "Instruction issue logic for pipelined supercomputers," Proc. 11th Symposium on Computer Architecture (June) pp. 110-118.

EXAMINER V. Vu DATE CONSIDERED 1-29-01

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

#12

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Attorney Docket No.: 12172-004530US	Application No.: 09/057,861
	Applicant: Howard G. Sachs	
	Filing Date: April 9, 1998	Group: ~~2784~~ 2154

Reference Designation — **U.S. PATENT DOCUMENTS** — Page 1

Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub-class	Translation (Yes/No)
VV AA	0 496 928	08/05/92	EPO			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

VV AB	De Gloria et al., "A Programmable Instruction Format Extension to VLIW Architectures", Proceedings: Computer Systems and Software Engineering The Hague, May 4-8, 1992 pp:35-40
VV AC	Dorozhevets et al., "The El'brus-3 and MARS-M: Recent Advances In Russian High-Performance Computing", The Journal of Supercomputing 6(1):5-48 (1992)
VV AD	Kato et al., "Delayed Instruction Execution On A Long Instruction Word (LIW) Computer", Systems and Computers in Japan 23(14):13-22 (1992)

EXAMINER V. Vu — DATE CONSIDERED 1-29-01

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.